



## **Supplemental Application Data Sheet**

### **Application Information**

Application number::	<u>10/750,375</u>
Filing Date::	<u>12/31/03</u>
Application Type::	Regular
Subject Matter::	Utility
Suggested Group Art Unit::	N/A
CD-ROM or CD-R?::	None
Sequence submission?::	None
Computer Readable Form (CRF)?::	No
Title::	LOW POWER TRI-LEVEL DECODER CIRCUIT
Attorney Docket Number::	08211/0200346-US0/P05774
Request for Early Publication?::	No
Request for Non-Publication?::	Yes
Total Drawing Sheets::	6
Small Entity?::	No
Petition included?::	No
Secrecy Order in Parent Appl.?::	No

### **Applicant Information**

Applicant Authority Type::	Inventor
Primary Citizenship Country::	US
Status::	Full Capacity
Given Name::	Eric
Middle Name::	David
Family Name::	Blom
City of Residence::	Wakefield
State or Province of Residence::	MA
Country of Residence::	US
Street of mailing address::	44 Church Street

City of mailing address:: Wakefield  
State or Province of mailing address:: MA  
Postal or Zip Code of mailing address:: 01880

**Correspondence Information**

Correspondence Customer Number:: 38845

**Representative Information**

Representative Customer Number:: 38845

**Assignee Information**

Assignee name:: National Semiconductor Corporation  
Street of mailing address:: 2900 Semiconductor Drive  
City of mailing address:: Santa Clara  
State or Province of mailing address:: CA  
Postal or Zip Code of mailing address:: 95051-8090